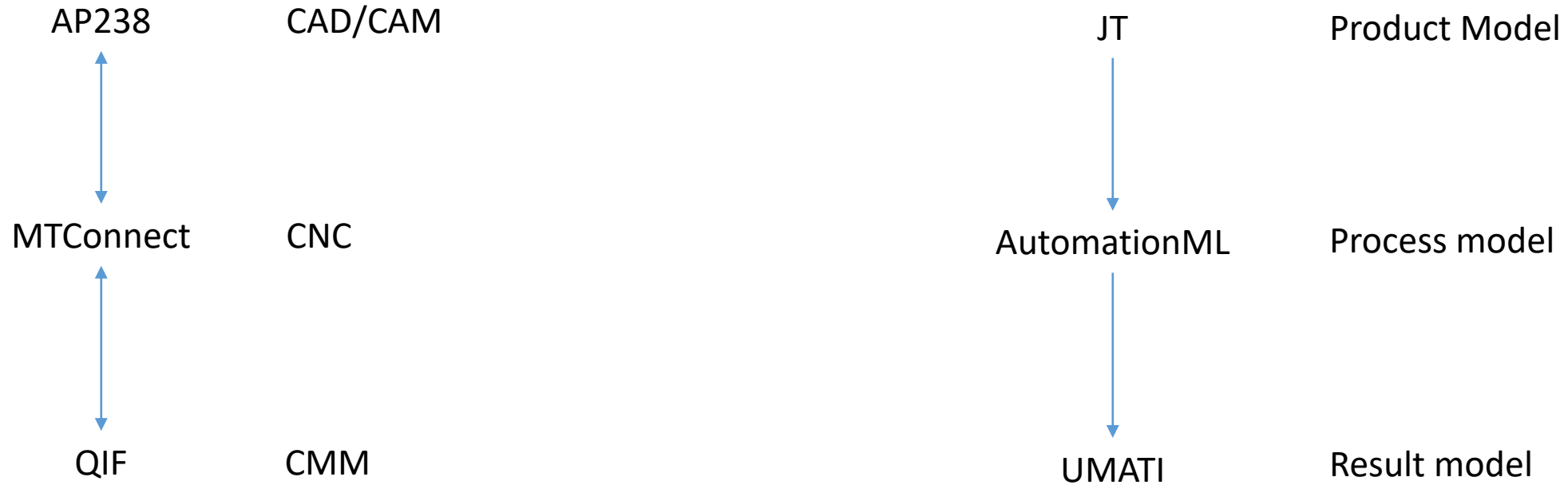


# Digital Threads for Quality Control

UUID Conference Call

May 10, 2023

# Manufacturing Digital Threads

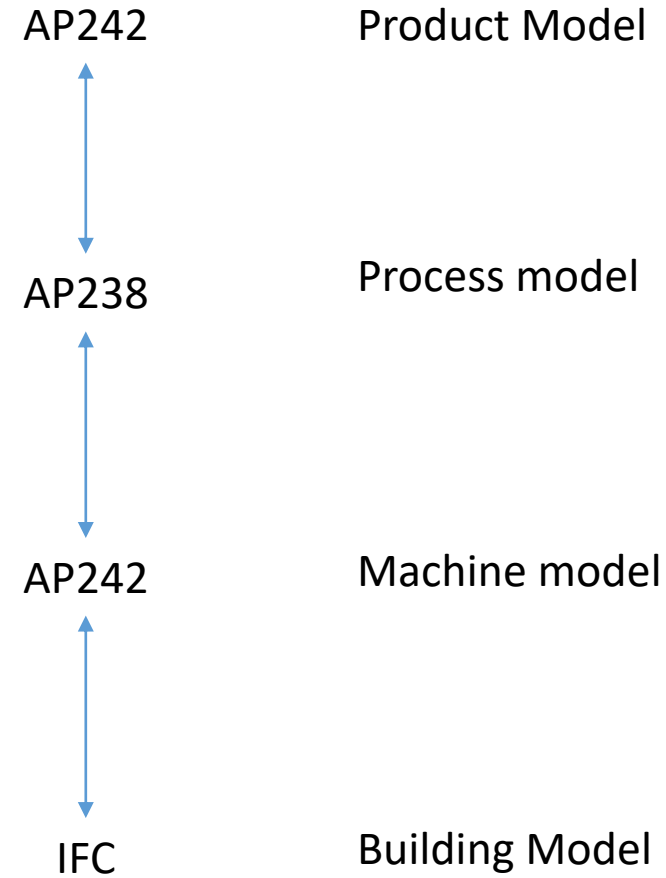
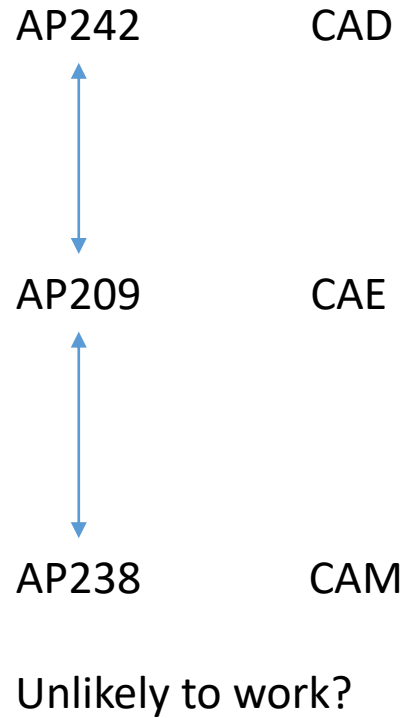


Is this 23247-2 or a thread?

Is this the Asset Administration Shell solution?

Links bi-directional because of UUID's?

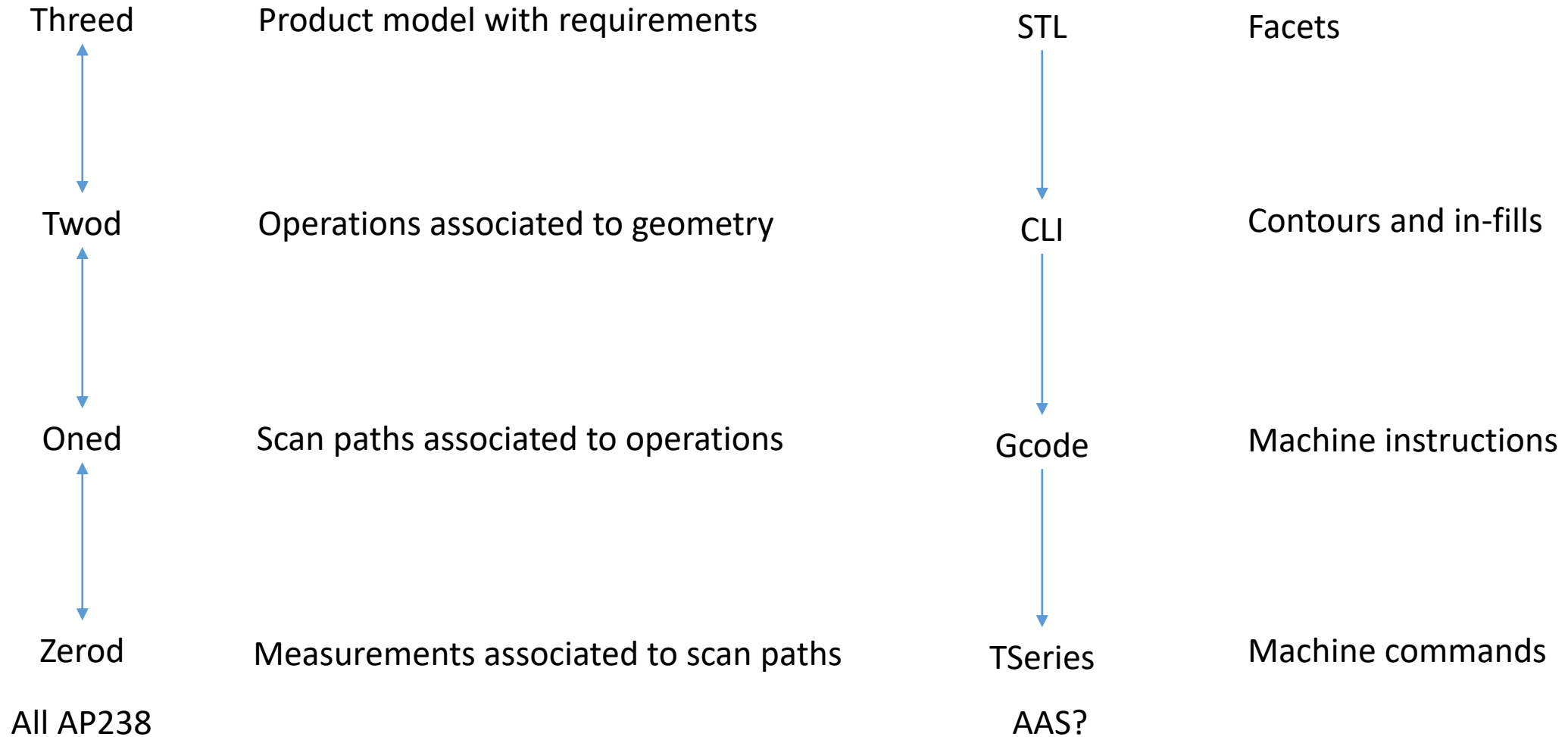
# Digital Threads



Common geometry

CHIPS business case?

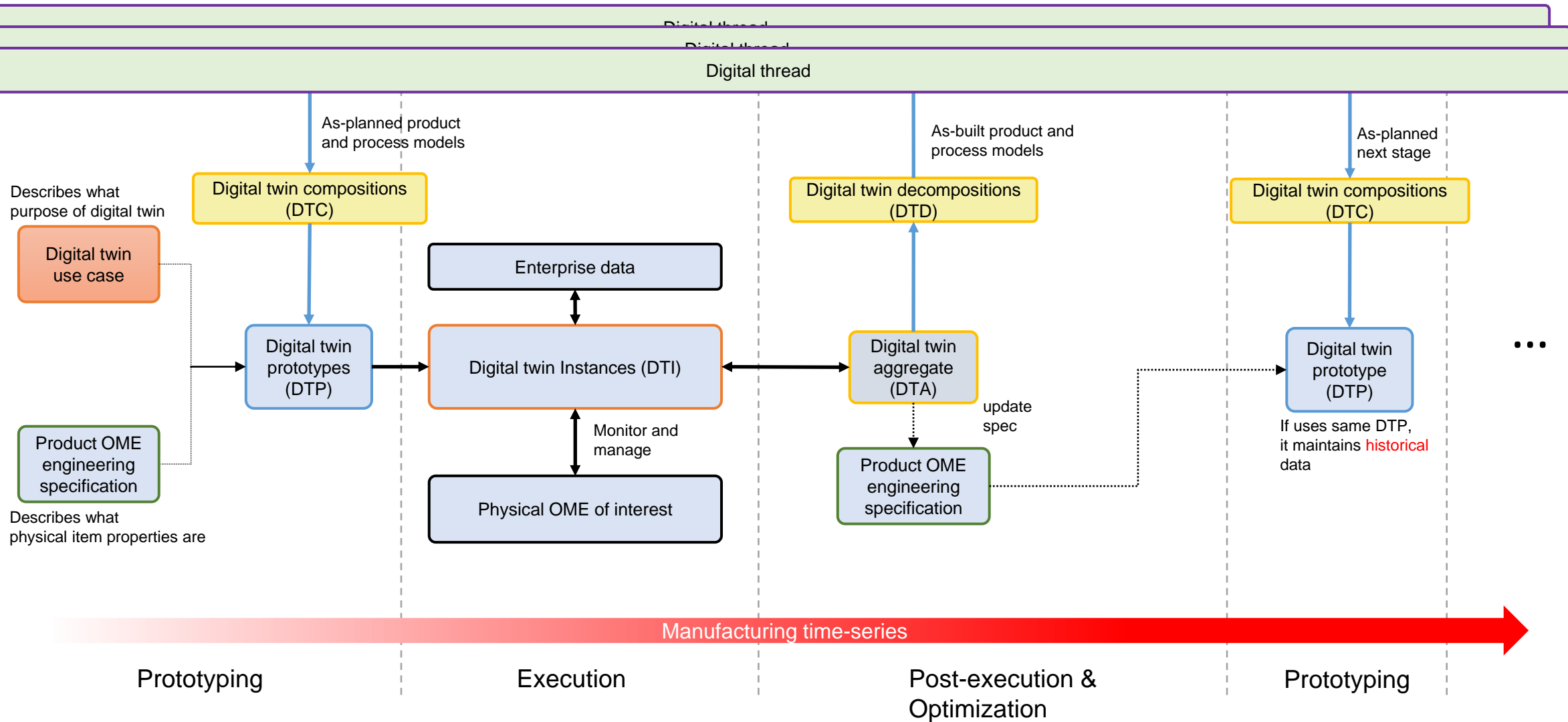
# Powder Bed Digital Threads



# What is a digital thread?

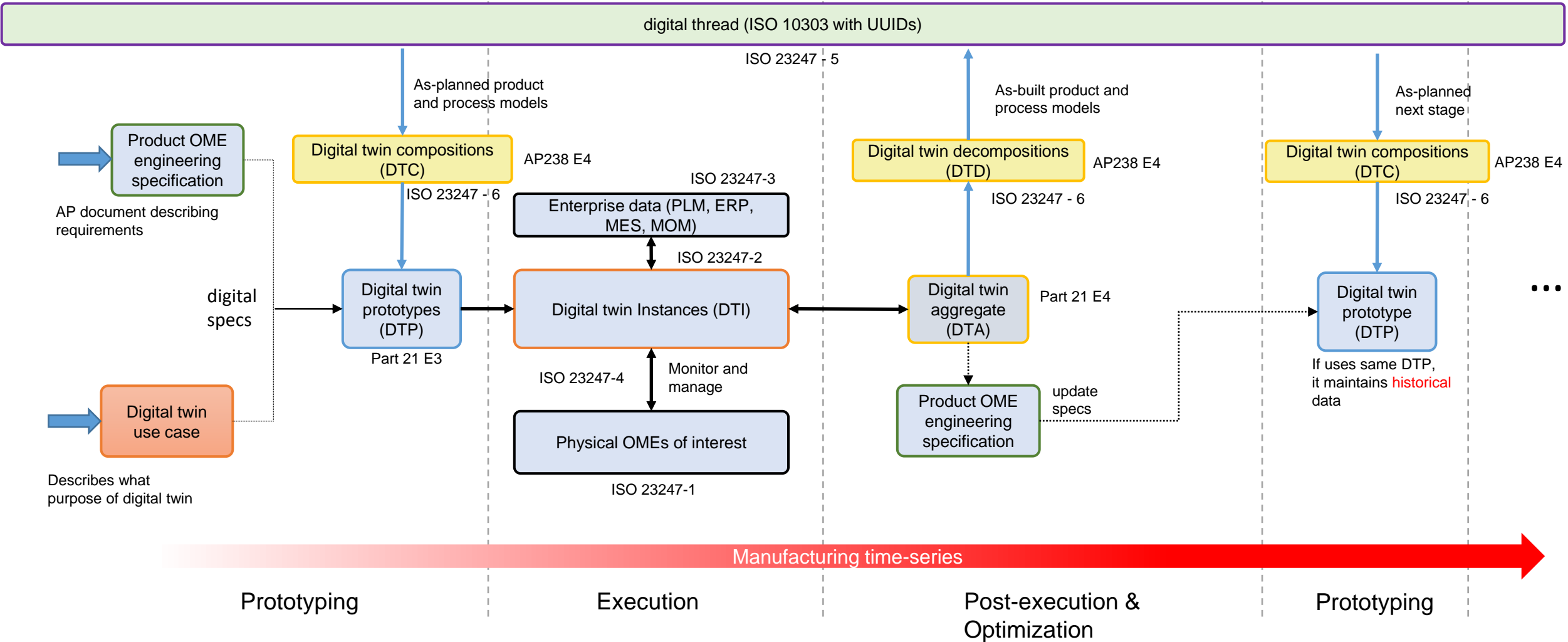
- A digital thread enhances quality control by enabling better comparisons between as-planned data and as-executed results
- A digital thread links data, instead of transforming data, so it does not require a translation of the master model
  - AP238 & AP242 link at the common entity level
  - STEP & IFC link at the common geometry level
  - STEP, MTC and QIF link at the common concept level
  - AAS links at the common application level

*threads connecting necessary and sufficient life cycle data*



Roadmap for digital twin – digital thread manufacturing

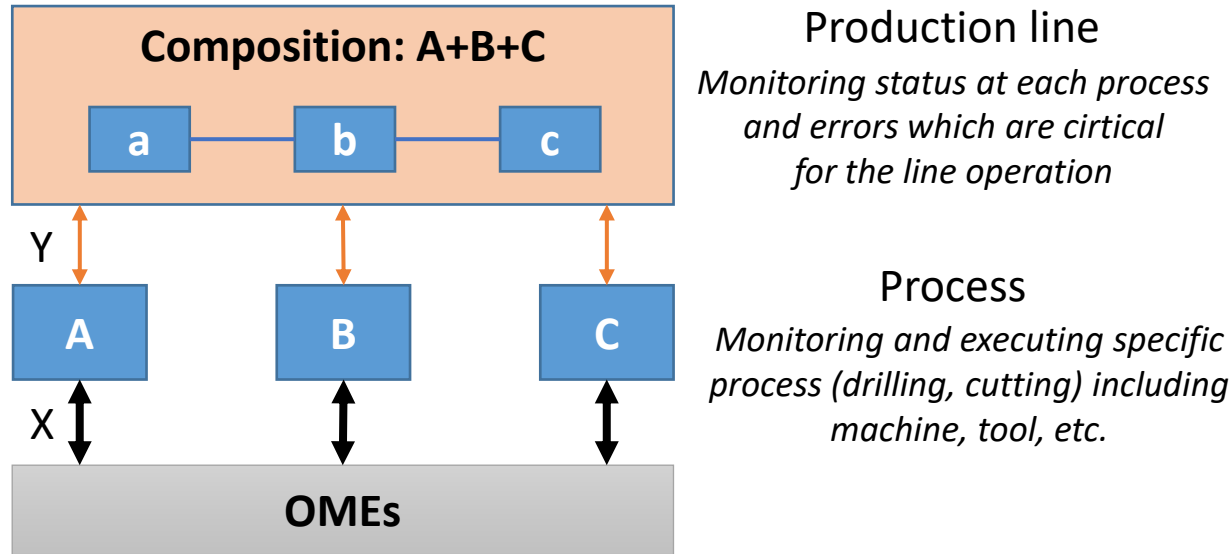
*thread connecting necessary and sufficient life cycle data*



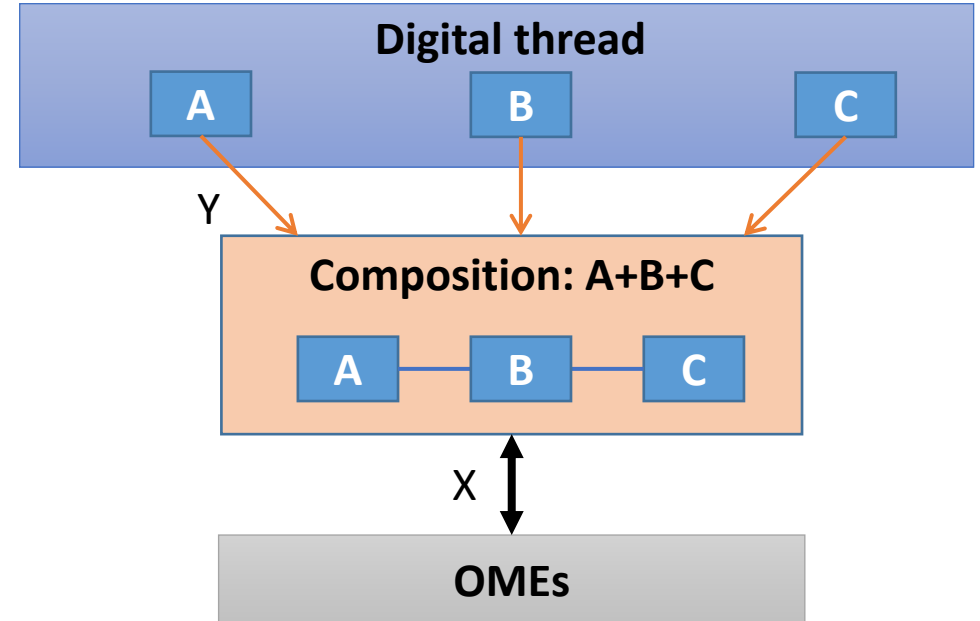
Roadmap for digital twin – digital thread manufacturing

# Composition by merger and/or orchestration

4 twins: A, B, C, Composition(A+B+C)



- Focus: gather information from different twins and using it to represent at a glance
- Need to define: how to select information and gather from other twins (protocol aspect)
- Operation/user oriented purpose



- Focus: gather information from different assets and merge data to create digital twin for specific purpose
- Need to define: how to construct digital twin information model from different data sources (information model aspects)
- Manufacturing/Product oriented purpose